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Docket #	P030.04.CIP.24+	By:	THD3	Faxing Date	August , 09, 2005
Ser. No:	10/765,406	Filing Date:	01/26/2004	Inv(s)	Darrell Rinerson et al.
Title:	Memory Array with High Temperature Wiring				

The following is being transmitted to the U.S. Patent Office:

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